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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

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**APPLICANTS: Nanjo-Ryota; Sugatani Shinji; Nakai Satoshi;						
**CONTINUING DATA VERIFIED: J. M. J. NONE						
** FOREIGN APPLICATIONS VERIFIED: J. M. J. JAPAN 2001-067164 03/09/2001 JAPAN 2001-256108 08/27/2001						
PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>				
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiner's initials J. M. J.			ATTORNEY DOCKET NO 030200			
TITLE : Semiconductor device and its manufacture method						
U.S. DEPT. OF COMM./PAT. & TM-PTO-436L(Rev. 12-94)						

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner	DRAWING	
Amount Due	Date Paid		Sheets Drwg.	Figs. Drwg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE		
Application Examiner				
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